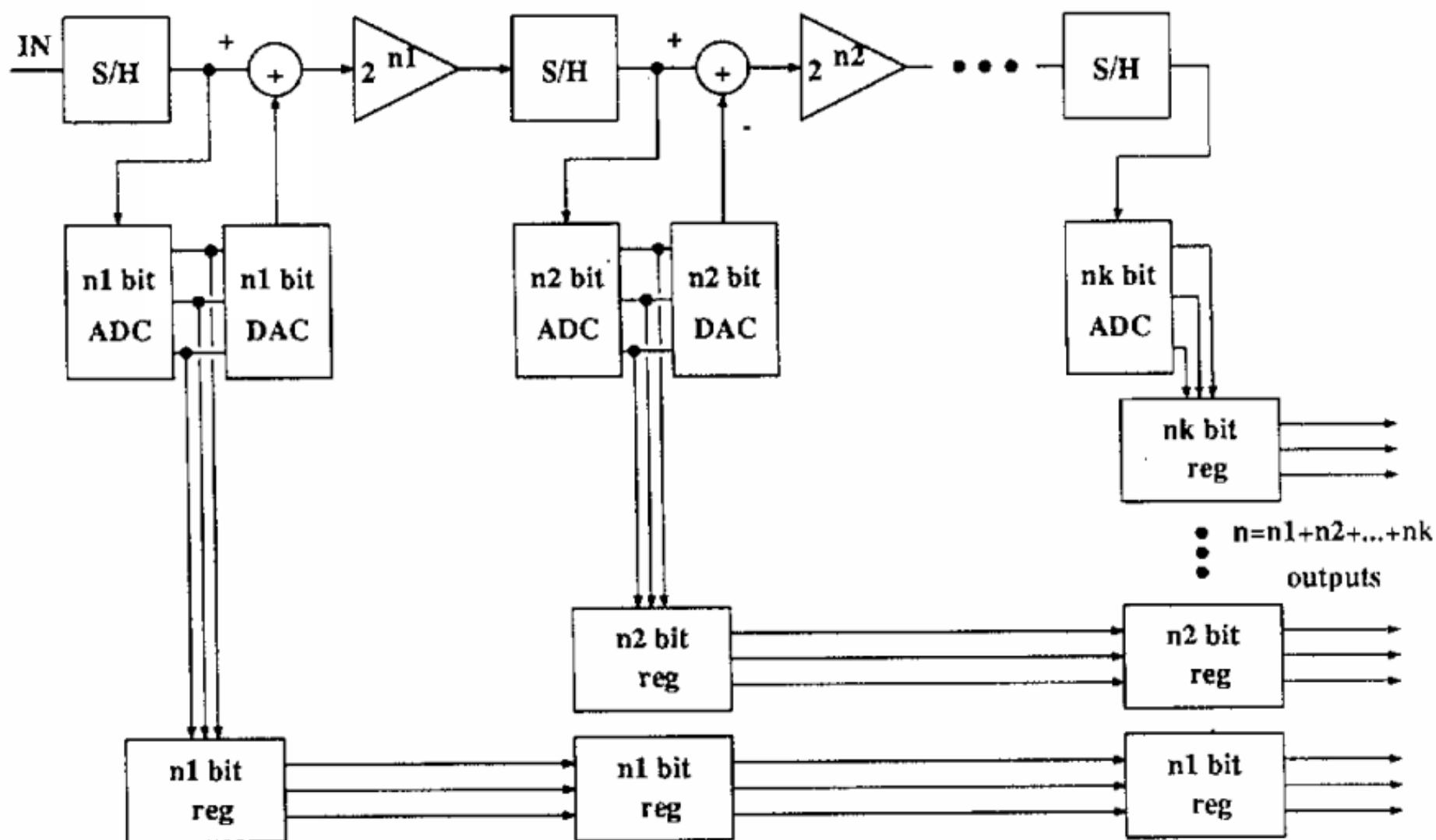


# BENCHMARK FOR PIPELINED ADC IN CMOS

ISSCC 1987



## A Pipelined 5MHz 9B ADC

TWO STEP FLASH converters and other schemes were common for high speed ADC at the time, but this paper showcased the exploitation of the inherent sampling action of switched capacitor techniques to provide a pipelining action, allowing the converter to process multiple samples at once, and effectively decouple the conversion rate from the conversion time. This allowed a significant increase in conversion speed while maintaining reasonable power and area limits. Pipelined ADCs have come to be one of the dominant converter architectures in the high speed arena.