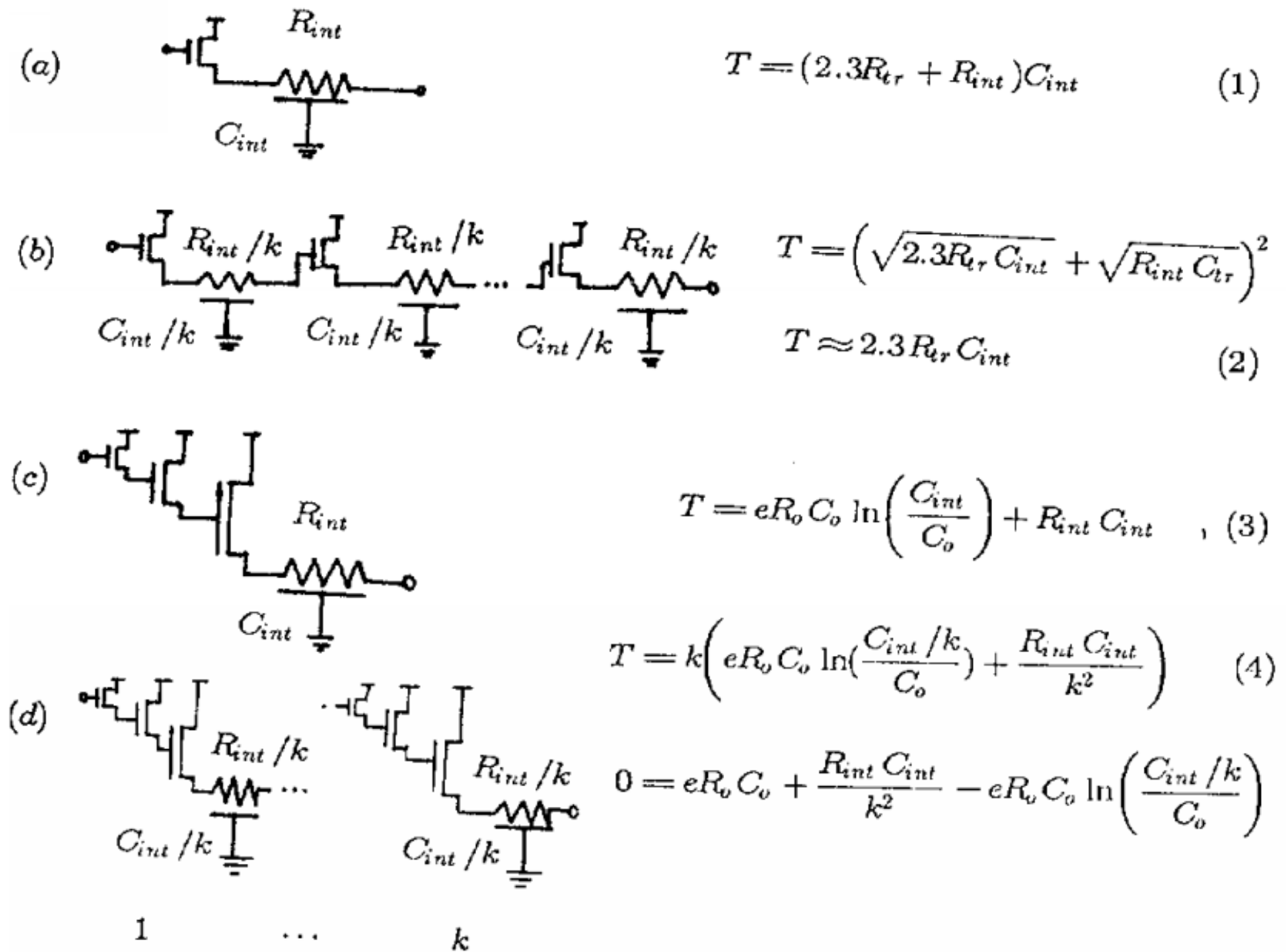


WIRE RC AND IMPROVED INTERCONNECT SOLUTIONS

ISSCC 1984



Optimal Interconnect Circuits for VLSI

WITH THE PUSH towards ever higher operating frequencies and with chip sizes continuing to increase, on-chip interconnect delay started to become a serious problem in the 1980s. This paper described the introduction of interconnect repeaters in order to speed up cross-chip signal wires. These repeater techniques are now used everywhere in today's VLSI designs, with modern chips employing tens or hundreds of thousands of repeaters.