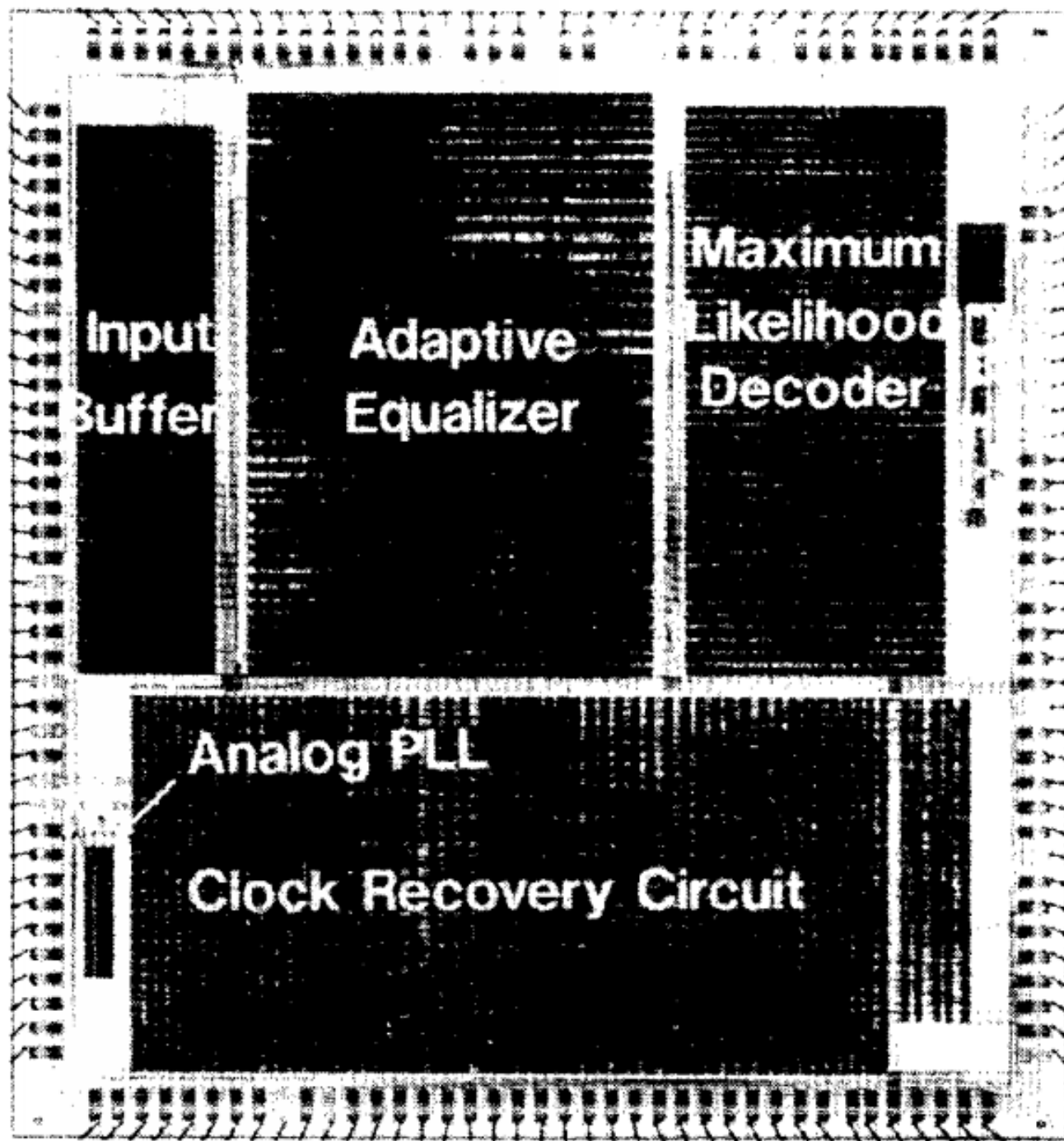


# CMOS READ CHANNEL

ISSCC 1993



## An Adaptive Equalizing Maximum Likelihood Decoding LSI for Magnetic Recording Systems

1993 MARKED THE first appearance of a CMOS read channel for magnetic recording, with a single-chip channel aimed at magnetic tape systems by Tanaka et al.

S. Tanaka<sup>1</sup>, H. Kojima<sup>1</sup>, Y. Okada<sup>1</sup>, F. Nakazawa<sup>2</sup>, T. Hikage<sup>2</sup>, H. Matsushige<sup>1</sup>,  
M. Kosuge<sup>1</sup>, H. Miyasaka<sup>1</sup>, T. Takashi<sup>1</sup>, S. Hanamura<sup>1</sup>

<sup>1</sup> Hitachi, Ltd., Tokyo, Japan

<sup>2</sup> Hitachi VLSI Engineering Corp., Tokyo, Japan